

10/603383

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as First Class Mail, in an envelope addressed to:
Attention: Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: July 25, 2006

Signature

(Anthony G. Sitko)

Docket No.: 30205/39428
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Yun Y. Choi

Patent No.: 6,956,440

Issued: October 18, 2005

For: PLL HAVING A CONTROLLER FOR
DIVIDING VALUES OF A VCO

**Certificate
of Correction**
AUG 02 2006

**REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 CFR 1.322**

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted a typographical error which should be corrected.

In the Abstract:

At the cover page, Column 2, please replace the abstract to read as follows:

--A PLL can operate linearly at various frequencies by overlapping several VCOs and using a control circuit for controlling the several VCOs to select a VCO at a desired frequency. Accordingly, the PLL can automatically pre-compensate the frequency by using a control signal used in the PLL. As a result, a separate frequency compensation signal is not required. The PLL can be ~~configure~~ configured on a single chip when the VCO is contained within the PLL.--

The error was not in the application as filed by applicant; accordingly no fee is required.

AUG 2 2006

Patent No.: 6,956,440

Docket No.: 30205/39428

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

Dated: July 25, 2006

Respectfully submitted,

By 

Anthony G. Sitko

Registration No.: 36,278

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AUG 2 2006

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 1 of 1

PATENT NO. : 6,956,440
APPLICATION NO. : 10/603,383
ISSUE DATE : October 18, 2005
INVENTOR : Yun Y. Choi

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Abstract:

At the cover page, Column 2, please replace the abstract to read as follows:

--A PLL can operate linearly at various frequencies by overlapping several VCOs and using a control circuit for controlling the several VCOs to select a VCO at a desired frequency. Accordingly, the PLL can automatically pre-compensate the frequency by using a control signal used in the PLL. As a result, a separate frequency compensation signal is not required. The PLL can be configured on a single chip when the VCO is contained within the PLL.--

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